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FORM PTO-1449 (Modified ATTY. DOCKET NO. SERIAL NO. APR 1 2 2002 PUBLICA RPS920010132US1 10/015,921 LIST OF PATENTS AND ONS FOR RECEIVED APPLICANT'S INFOR **DISCLOSURE STATEM** APPLICANTE APPLICANTE 2100 Page 2 of 4 R. T. Bailis, et al. (Use several sheets if necessary) FILING DATE: GROUP: 12/10/2001 REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS EXAMINER** DOCUMENT FILING DATE INITIALS NUMBER DATE NAME CLASS **SUBCLASS** (IF APPRO.) 6 6 4B1 Feb. 20, 2001 Schultz, et al. 326 41 Aug. 13, 1999 2 0 9 8B1 Mar. 27, 2001 1 LaBerge 716 ŀ Jan. 21, 1998 6 2 1 6 9 7BI Apr. 3, 2001 Lien, et al. 326 41 May 25, 1999 6 2 9 8 9B1 Apr. 17, 2001 1 Vashi, et al. 716 3 Jun. 26, 1998 6 2 8 3 3B1 Apr. 17, 2001 Solomon, et al. 717 5 Dec. 11, 1998 6 Apr. 24, 2001 Stewart, et al. 703 25 Aug. 14, 1998 FOREIGN PATENT DOCUMENTS DOCUMENT Translation NUMBER DATE COUNTRY **CLASS SUBCLASS** YES NO OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) R. T. Bailis et al., U.S. Pending Patent Application Serial No. 10/016449 (docket RPS920010127US1), "Method and DD System for Use of a Field Programmable Gate Array Function within an Application Specific Integrated Circuit (ASIC) to Enable Creation of a Debugger Client within the ASIC" R. T. Bailis et al., U.S. Pending Patent Application Serial No. 10/016448 (docket RPS920010128US1), "Method and EE System for Use of a Field Programmable Function within an Application Specific Integrated Circuit (ASIC) to Access Internal Signals for External Observation and Control" **EXAMINER** DATE CONSIDERED 2/8/05 EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line

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